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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/601,876	06/24/2003	Hidetada Tokioka	009683-468	1311

7590

10/04/2005

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EXAMINER

WU, XIAO MIN

ART UNIT

PAPER NUMBER

2674

DATE MAILED: 10/04/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	10/601,876	TOKIOKA ET AL.	
	Examiner	Art Unit	
	XIAO M. WU	2674	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 24 June 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-14 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 7-14 is/are allowed.
- 6) ☒ Claim(s) 1,5 and 6 is/are rejected.
- 7) ☒ Claim(s) 2-4 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 24 June 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|----------------------------------------------------------------------------------------------------|-----------------------------------------------------------------------------|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1, 5 and 6 are rejected under 35 U.S.C. 102(b) as being anticipated by Suzuki (US Patent No. 6,369,786).

As to claim 1, Suzuki discloses a current supply circuit (2, Fig. 4) for supplying an output current according to an input voltage to a signal line, comprising: a current driving portion (2, Fig. 4) provided to supply said output current to said signal line, in which a passing current changes according to a voltage of a control node (e.g. S1, S2,...); a voltage holding portion (3, fig. 4) for holding the voltage of said control node; a current compensating portion (3, Fig. 4) for setting said control node to a voltage corresponding to a reference current by passing said reference current to said current driving portion in a first operation mode (e.g. pre-charging the signal line in the switching node) in which an input node is set to a predetermined initial voltage (e.g. pre-charging voltage V_t); and an input transmitting portion (C1, C2, ...Cx), in a second operation mode (e.g. applying current to the source from the current source 2) which is executed after said first mode (e.g. pre-charging) and in which said input node receives transmission of said input voltage, for changing the voltage of said control node in accordance with a change in the voltage of said input node between said first and second operation modes (see col. 5, lines 27-40).

As to claim 5, Suzuki discloses the output current is supplied to a current-driven light emitting element (ScE1), and said input voltage is set to a level corresponding to display luminance of said current-driven light emitting element (see Fig. 6).

As to claim 6, Suzuki discloses a switch portion (S1, S2,...) provided between said current driving portion and said signal line and turned on in said second operation mode, wherein in an ON period of said switch portion, a voltage of said input node is maintained at said predetermined initial voltage (e.g. pre-charging voltage V_t) for a predetermined period and, after that, said input voltage is transmitted to said input node (see Fig. 6).

Allowable Subject Matter

3. Claims 2-4 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

4. Claims 7-13 are allowed.

5. The following is a statement of reasons for the indication of allowable subject matter:

None of the prior art of the record, alone or in combination, discloses or fairly suggests the limitation of "said signal line is electrically coupled to a first voltage at least in said second operation mode, said current driving portion has a first transistor, electrically coupled between a second voltage and a first node, having a gate coupled to said control node, said voltage holding portion has a first capacitive element connected between said control node and said second voltage, said current compensating portion has: a second transistor electrically coupled between said first node and a line for supplying said reference current and turned on in said first operation mode; and a third transistor electrically coupled between said first node and said control node

Art Unit: 2674

and turned on in said first operation mode, said input transmitting portion has a second capacitive element connected between said input node and said control node, and said current supply circuit further comprises a fourth transistor electrically coupled between said first node and said signal line and turned on at least in said second operation mode” as required in claim 2.

6. None of the prior art of the record, alone or in combination, discloses or fairly suggests the limitation of “each of said pixels includes a drive circuit for supplying a current according to said data current transmitted via the corresponding data line in an active period of the corresponding scan line to said current-driven light emitting element and continuously supplying a current corresponding to said data current to said current-driven light emitting element also in an inactive period of said corresponding scan line” in combination with other limitation as a whole as required in claim 7.

Conclusion

7. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. The US Patents 5,719,589, 6,169,528, 6,351,255, 6,414,661, 6,707,438, 6,747,617, 6,943,761, 2004/0061670 are cited to teach a light emitting display device.

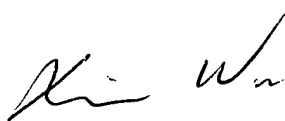
8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to XIAO M. WU whose telephone number is 571-272-7761. The examiner can normally be reached on 6:30 am to 4:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, PATRICK EDOUARD, can be reached on 571-272-7603. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Art Unit: 2674

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

X.W.
September 29, 2005



XIAO M. WU
Primary Examiner
Art Unit 2674